

REMARKS

All of the objections have been cured by making the changes suggested by the Examiner. Claim 6 properly calls for a carry evaluating circuit which has antecedent basis in claim 1.

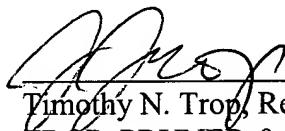
With respect to Figure 14 and the question about what is the source of a second input to each of the gates in element 301, it should be noted that "u" is dual rail coded and this forms the two inputs to the NAND and NOR gates in item 1301.

The elements of claims 13 and 23 are shown in Figure 6.

In view of these remarks, the application should now be in condition for allowance.

Respectfully submitted,

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